FIG. 1

20

Rpi1　Cpi1　CC1　CC2　Cpi2　Rpi2
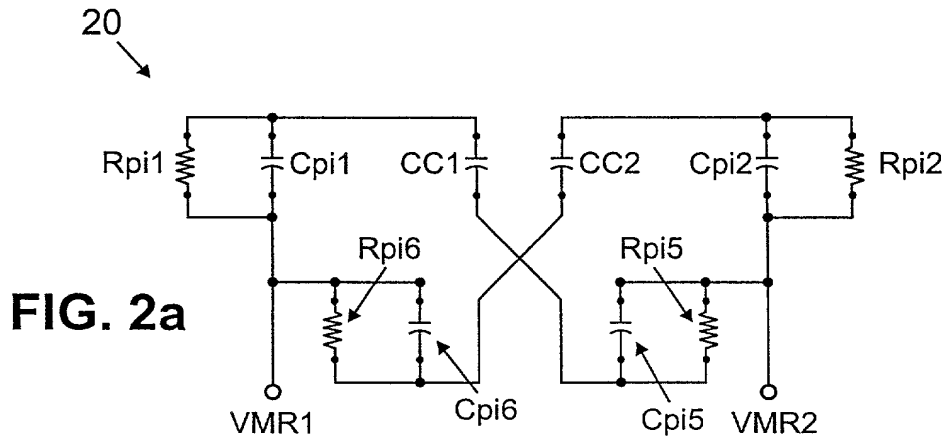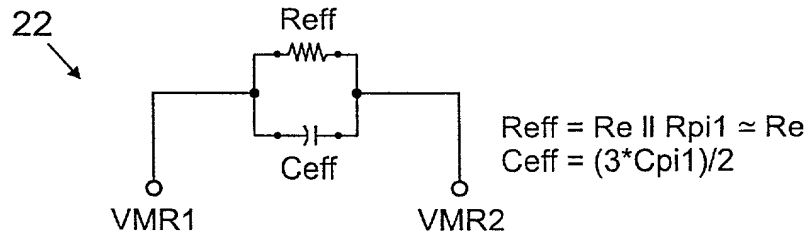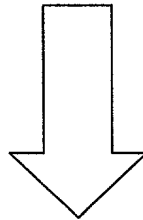
Rpi6　　　　　　　Rpi5

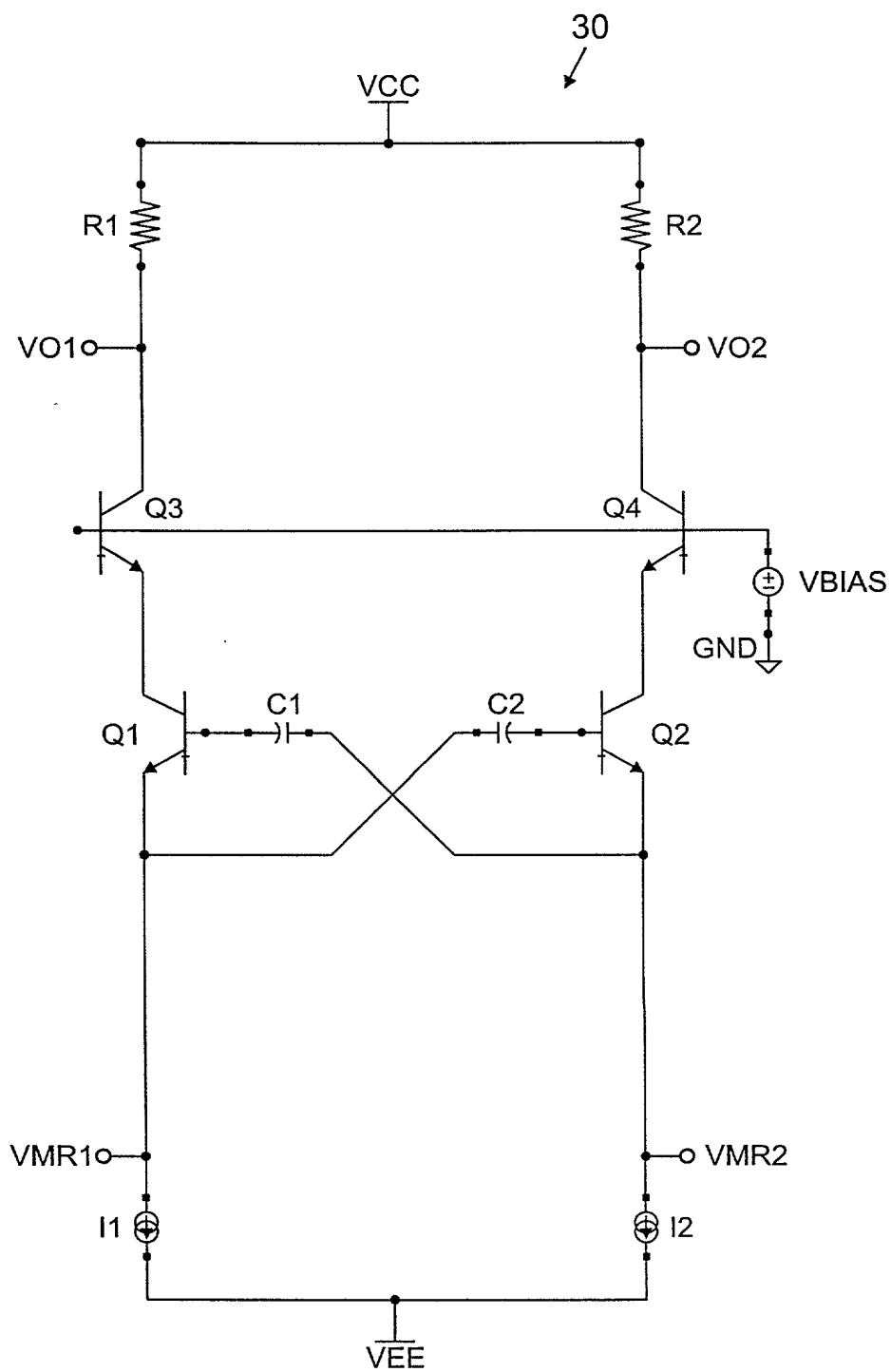VMR1　　Cpi6　　Cpi5　VMR2

FIG. 2a

EQUIVALENT INPUT CIRCUIT

$CC1 \gg Cpi1$ & $CC1 \gg Cpi2$
$CC2 \gg Cpi1$ & $CC2 \gg Cpi2$
$CC1 \gg Cpi5$ & $CC1 \gg Cpi6$
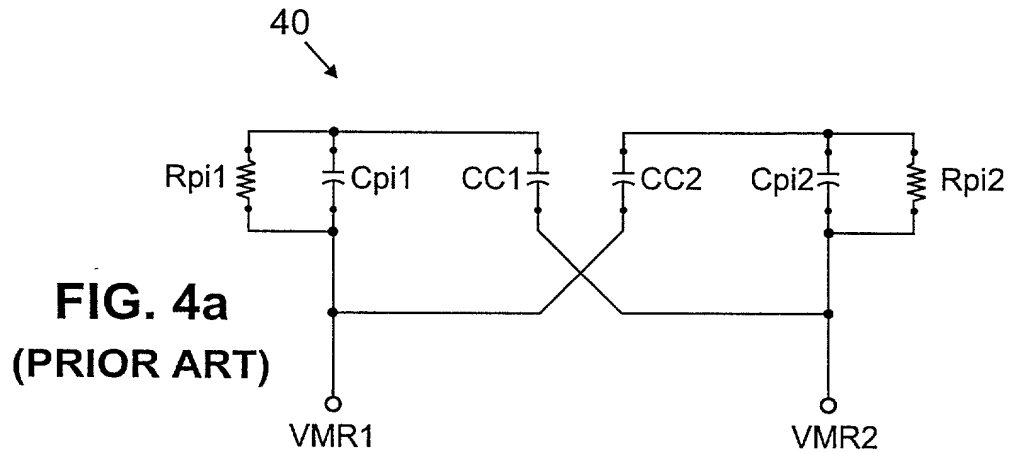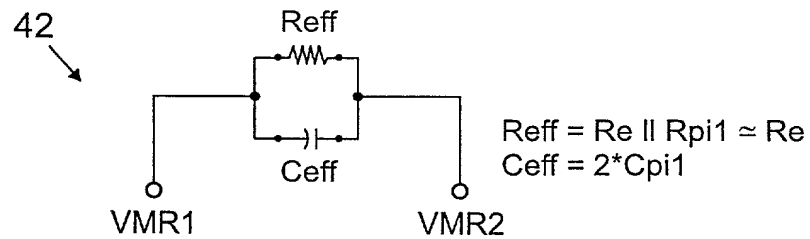$CC2 \gg Cpi5$ & $CC2 \gg Cpi6$

22

Reff

Ceff

VMR1　　　　　　VMR2
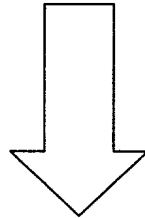
$Reff = Re \parallel Rpi1 \simeq Re$
$Ceff = (3*Cpi1)/2$

SIMPLIFIED CIRCUIT

FIG. 2b

30

VCC

R1

R2

VO1

VO2

Q3

Q4

VBIAS

GND

Q1

C1

C2

Q2

VMR1

VMR2

I1

I2

VEE

FIG. 3
(PRIOR ART)

40

FIG. 4a
(PRIOR ART)

VMR1   VMR2

EQUIVALENT INPUT CIRCUIT

CC1 >> Cpi1 & CC1 >> Cpi2
CC2 >> Cpi1 & CC2 >> Cpi2

42

Reff

Ceff

$Reff = Re \parallel Rpi1 \simeq Re$
$Ceff = 2*Cpi1$

VMR1   VMR2

SIMPLIFIED CIRCUIT

FIG. 4b
(PRIOR ART)

FREQUENCY REQPONSE
RMR ONLY 50

■: REV 2
■: REV 1

FIG. 5

FIG. 6